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APPLICANTS

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** CONTINUING DATA ***** *None NL*

** FOREIGN APPLICATIONS ***** *None NL*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY MD	SHEETS DRAWING 13	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>None NL</i> Examiner's Signature Initials				

ADDRESS

33233
 LAW OFFICE OF CHARLES W. PETERSON, JR.
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TITLE

Computer aided design method and apparatus for modeling and analyzing on-chip interconnect structures

☐ All Fees☐ 1.16 Fees (Filing)